

BIT 7	BIT 6	BIT 5	BIT 4	BIT 3	BIT 2	BIT 1
DATA	DMA	DMA	DATA	P _{1,6,5}	P _{7,6,4}	P _{7,5,4}

FIGURE 1

1	1	0	1	0	1	0
---	---	---	---	---	---	---

FIGURE 3

0	1	0	0	0	1	0
---	---	---	---	---	---	---

FIGURE 4

0	1	1	0	0	1	1
---	---	---	---	---	---	---

FIGURE 5

0	1	0	0	0	1	0
---	---	---	---	---	---	---

FIGURE 6

0	1	1	1	0	0	0
---	---	---	---	---	---	---

FIGURE 8

0	1	0	1	0	0	0
---	---	---	---	---	---	---

FIGURE 9

IDENTIFY X BIT(S) IN THE
UN-PROGRAMMED STATE, WHERE
"X" IS SUFFICIENT TO INTRODUCE
AN UNCORRECTABLE ERROR IN
THE WORD

20
✓

22

SWITCH THE X BIT(S) FROM THE
UN-PROGRAMMED STATE TO THE
PROGRAMMED STATE

24

FIGURE 2

IDENTIFY A SINGLE BIT THAT IS
IN THE UNPROGRAMMED STATE

32

GENERATE A SECOND WORD, WHEREIN
ALL OF THE DATA BITS IN THE SECOND
WORD ARE IN THE UN-PROGRAMMED
STATE EXCEPT FOR THE BIT THAT
CORRESPONDS TO THE SINGLE BIT

30
✓

34

OVERWRITE THE FIRST WORD WITH
THE SECOND WORD

36

FIGURE 7